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5649-1161

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Examiner: Maria F. Guerrero

Group Art Unit: 2822

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Attorney's Docket No. 5649-1161

APR 1 1 2008

PATENT

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hyun-Chul Kim Serial No.: 10/663,967

Confirmation No.: 2684 Group Art Unit: 2822

Filed: September 16, 2003

Examiner: Maria F. Guerrero

METHODS OF FABRICATING INTEGRATED CIRCUIT DEVICES HAVING FUSE STRUCTURES INCLUDING BUFFER LAYERS

Date: April 11, 2006

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## RESPONSE TO SECOND NOTICE OF NON-COMPLIANT **AMENDMENT**

Sir:

In response to the Second Notice of Non-Compliant Amendment ("the Notice") in the above referenced matter dated March 13, 2006, Applicant provides only the corrected "listing of claims" in accordance with 37 C.F.R. 1.121.

It is not believed that an extension of time and/or additional fee(s), including fees for net addition of claims-are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

Amendments to the Claims are reflected in the listing of claims, which begins on page 3 of this paper.

Remarks begin on page 7 of this paper.